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# Community Dentistry Soben Peter 5th Edition : 4th Edition

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Additionally, a high dielectric constant leads to a decrease in the performance of a conventional interconnection (e.g., via) between two wiring layers, as a function of the distance between the conductors. Thus, conventional printed circuit board designs include via structures in several layers to minimize the effects of high dielectric constant materials. In addition, dense materials having a high dielectric constant are employed to minimize these effects. Recently, researchers have sought to provide a porous printed circuit board in order to (1) increase the electrical conductivity of the board, and/or (2) decrease the dielectric constant of 3da54e8ca3

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